

Form 1449*	Atty. Docket No.: 303.377US3	Serial No. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Kie Y. Ahn et al.	
	Filing Date: Herewith	Group: Unknown

U. S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Applicable
<u>MG</u>	4,981,820	01/01/1991	Renlund, G.M., et al.	501	39	07/28/89
<u>MG</u>	5,103,288	04/07/1992	Sakamoto, M., et al.	357	71	03/27/91
<u>MG</u>	5,177,567	01/05/1993	Klersy, P.J., et al.	257	4	07/19/91
<u>MG</u>	5,180,694	01/19/1993	Renlund, G.M., et al.	501	12	08/30/90
<u>MG</u>	5,380,553	01/10/1995	Loboda, M.J.	427	226	12/24/90
<u>MG</u>	5,470,802	11/28/1995	Gnade, B.E., et al.	437	238	05/20/94
<u>MG</u>	5,525,857	06/11/1996	Gnade, B., et al.	313	309	08/19/94
<u>MG</u>	5,527,737	06/18/1996	Jeng, S.	437	195	05/27/94
<u>MG</u>	5,530,581	06/25/1996	Cogan	359	265	05/31/95
<u>MG</u>	5,598,026	01/28/1997	Kapoor, A.K., et al.	257	634	06/05/95
<u>MG</u>	5,789,819	08/04/1998	Gnade, B.E., et al.	257	759	06/07/95
<u>MG</u>	5,818,071	10/06/1998	Loboda, M.J., et al.	257	777	02/02/95
<u>MG</u>	5,821,621	10/13/1998	Jeng, S.	257	759	10/10/96
<u>MG</u>	5,827,780	10/27/1998	Hsia, L.C., et al.	438	692	04/01/96
<u>MG</u>	6,022,625	02/08/2000	Bremmer, J.N., et al.	428	447	08/03/98
<u>MG</u>	6,054,751	04/25/2000	Ichikawa, K., et al.	257	532	09/17/97

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<u>MG</u>	0684642	11/29/1995	European			

OTHER DOCUMENTS

**Examiner Initial	(Including Author, Title, Date, Pertinent Pages, Etc.)
<u>MG</u>	Anand, M., et al., "NURA: A Feasible, Gas-Dielectric Interconnect Process", <u>Symp. on VLSI Technology Digest of Technical Papers</u> , 82-83, (1996)
<u>MG</u> ✓	Anderson, R., et al., "Porous Polycrystalline Silicon: A New Material for MEMS", <u>J. Microelectromechanical Systems</u> , 3, 10-18, (March 1994)

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*Substitute Disclosure Statement Form (PTO-1449)

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(Including Author, Title, Date, Pertinent Pages, Etc.)

MG	Singh, A., et al., "Porous Silicon Oxycarbide Glasses", <u>J. Am. Ceram. Soc.</u> , 79, 2696-2704, (1996)
MG	Togo, M., et al., "A Gate-side Air-gap Structure (GAS) to Reduce the Parasitic Capacitance in MOSFETs", <u>Symp. on VLSI Technology Digest of Technical Papers</u> , 38-39, (1996)
MG	Watanabe, H., et al., "A Novel Stacked Capacitor with Porous-Si Electrodes for High Density DRAMs", <u>1993 Symposium on VLSI Technology, Digest of Technical Papers</u> , Kyoto, Japan, 17-18, (1993)

Examiner <i>Myra Guerrero</i>	Date Considered <i>5-31-02</i>
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Sheet 1 of 1

Form 1449*

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INFORMATION DISCLOSURE STATEMENT
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Applicant: Kie Y. Ahn et al.

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**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
						If Appropriate
MG	5,448,015	01/30/1996	Havemann, R.H., et al.	174	68.3	05/20/94
MG	5,494,858	01/27/1996	Gnade, B.E., et al.	437	231	06/07/94
MG	5,504,042	04/02/1996	Cho, C., et al.	437	247	06/23/94

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**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
MG	0775669	✓ 05/28/1997	European	C01B	13/32		
MG	0849796	✓ 06/24/1998	European	H01L	23/532		
MG	10-150033	✓ 06/02/1998	Japan	H01L	21/312 with English Abstract		

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